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Inventors: **LEONEL ERNESTO ENRIQUEZ**

For: **Controlled Switching Mechanism for Accomplishing Soft Polarity Reversal, Ring-trip Filtering, and Line Voltage Measurements in a Subscriber Line Interface Circuit Using Common Capacitor**

Enclosed are:

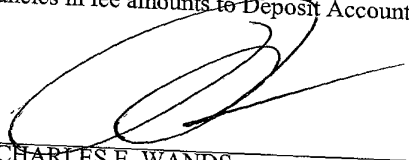
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- ☐ A certified copy of a _____ application.
- ☒ A declaration and power of attorney.
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October 11, 2000
Date _____


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50089

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): **LEONEL ERNESTO ENRIQUEZ**

Invention: **CONTROLLED SWITCHING MECHANISM FOR ACCOMPLISHING
SOFT POLARITY REVERSAL, RING-TRIP FILTERING, AND LINE
VOLTAGE MEASUREMENTS IN A SUBSCRIBER LINE INTERFACE CIRCUIT
USING COMMON CAPACITOR**

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TO WHOM IT MAY CONCERN:

Be it known that I, **LEONEL ERNESTO ENRIQUEZ**, resident of the United States of America, residing at 3039 A1A #2E, Melbourne Beach, Florida, 32951, have invented new and useful improvements in **CONTROLLED SWITCHING MECHANISM FOR ACCOMPLISHING SOFT POLARITY REVERSAL, RING-TRIP FILTERING, AND LINE VOLTAGE MEASUREMENTS IN A SUBSCRIBER LINE INTERFACE CIRCUIT USING COMMON CAPACITOR**, of which the following is a specification.

CONTROLLED SWITCHING MECHANISM FOR ACCOMPLISHING SOFT
POLARITY REVERSAL, RING-TRIP FILTERING, AND LINE
VOLTAGE MEASUREMENTS IN A SUBSCRIBER LINE INTERFACE
CIRCUIT USING COMMON CAPACITOR

FIELD OF THE INVENTION

The present invention relates in general to communication systems and components, and is particularly directed to a new and improved controllably switched
5 circuit architecture for use with a subscriber line interface circuit to selectively provide for soft polarity reversal detection, ring-trip filtering, and line voltage measurement by means of a common capacitor.

BACKGROUND OF THE INVENTION

10 Because wireline parameter and signaling requirements of telecommunication systems vary widely from country to country, manufacturers of various types of system equipments and components, such as multi-function line cards, have attempted to incorporate as
15 many functions as possible, into what may be termed a 'quasi-universal' subscriber line interface circuit (SLIC), and provide the user with the ability to

externally program the SLIC, so that it will conform with a given end use functionality.

Programming of such a SLIC to meet the parametric requirements of a particular installation may require the connection of one or more external components, such as resistors and/or capacitors, to respective circuits dedicated to respectively different functions. For this purpose, it has been conventional practice to provide the SLIC with separate pins for connecting each component to an associated circuit node. Not only can this result in a card configuration that has a substantially elevated pin count, but using a separate component for each circuit function connection means that there must be sufficient circuit board real estate to accommodate all the components.

As non-limiting examples, three of the more commonly employed circuit functions that entail the use of externally programmed components are: 1- noiseless or soft polarity reversal of the tip and ring terminals connected to the telephone; 2- preventing false detection (ring trip) when ringing the phone; and 3- the ability to provide a measure or estimate of the length of the wireline pair that connects the line card to the phone. Each of these functions customarily requires the connection of an external capacitor for its respective circuit and, in accordance with standard practice,

described above, it has been customary to provide the SLIC with three separate pins and circuit board space for connecting three separate external capacitors with their respective signaling functions.

5

SUMMARY OF THE INVENTION

In accordance with the present invention, this hardware complexity problem is significantly reduced by recognizing that multiple circuit functions of the card which use the same type of external programming component
10 may either not be necessary or they do not have to occur at the same time. This means that respectively different circuit functions can be implemented using the same external component, by controllably connecting that component with a respectively different signaling circuit
15 as its function is required.

Pursuant to the invention, such selective component-to-function association is accomplished by integrating each of a plurality of different function-associated coupling circuits with a controlled switching circuit.
20 The coupling circuits are controllably enabled/disabled in association with the particular circuit required, while the controlled switching circuit is adapted to selectively connect each coupling circuit with the same externally programmed component. Through combined control
25 of the operation of the coupling circuits and operation

of the controlled switching circuit, respectively different circuit functions of the SLIC can be implemented with the same external component.

For purposes of providing a non-limiting example,
5 the invention will be described for the case of selectively connecting a single external capacitor for use with each of a polarity reversal detection circuit, a ring trip detection circuit, and a line parameter measurement circuit. It should be understood, however,
10 that the invention is not limited to use with this circuit component or these particular functions.

In order to provide a capacitor for polarity reversal detection, the controllably switched, external component-coupling circuit of the present invention
15 includes a polarity reversal detector coupling circuit that includes a pair of differentially connected controlled current flow devices, such as a bipolar transistor pair, having their emitter-collector paths coupled between a current source and respective ports of
20 a polarity reversal detection circuit. The control terminal (base) of one transistor is coupled to receive a polarity reversal reference voltage, while the control terminal (base) of the other transistor is coupled to a polarity reversal current source and to a first port of
25 the controlled switching circuit. The differential transistor pair is continuously enabled so as to drive

the polarity reversing circuit for any of the three functional states for which the invention may be used, so as to ensure that both tip and ring terminals of the SLIC will be supplied with the required polarity voltage.

5 Whenever the polarity reversal function is to be employed, a closed circuit path is provided through the controlled switching circuit so as to connect the capacitor with the differentially coupled transistor pair of the polarity reversal coupling circuit. Also, the ring
10 trip detection coupling circuit and the line parameter measurement coupling circuit are controllably disabled. As a result, the application of a (soft) polarity reversal command will have its direction established by the polarity of the polarity reversal current source and
15 sensed by the polarity reversal circuit.

 The ring trip detection coupling circuit includes its own differentially connected transistor pair having their emitter-collector current flow paths coupled between a current source, and respective ports of a ring
20 trip detection logic circuit. This current source is further coupled to a controllably switch current by-pass sink. The collectors of these differentially coupled transistors are coupled to respective ports of a ring trip detection logic circuit. The base of one transistor
25 is coupled to receive a ring trip reference voltage, while the base of the other transistor is coupled to a

second port of the controlled switching circuit, to which a selectively enabled/disabled polarity reversal current source is coupled.

During ringing mode, the controlled switching
5 circuit is controlled so as to provide a closed circuit path therethrough that places the capacitor in circuit with the ring trip detection coupling circuit. In addition, the line parameter measurement coupling circuit is disabled. In the configuration, although the polarity
10 reversing circuit continues to be driven by its associated differential transistor pair, and thereby ensure that both tip and ring terminals of the SLIC are fed with the required polarity, a path thereto to the capacitor through the controlled switching circuit is
15 interrupted, so that the capacitor is connected exclusively to the ring trip detection coupling circuit to serve as a ring trip filter function.

The line parameter measurement coupling circuit includes a further differentially connected transistor
20 pair, whose emitter-collector paths are coupled between a loop voltage current source and respective ports of a loop voltage output logic circuit. The base of one transistor is coupled to receive a line reference voltage, while the base of the other transistor is
25 coupled to a third port of the switching circuit. This third port is coupled to a selectively enabled/disabled

loop_voltage current source and to the capacitor.

During loop voltage test mode, the switches of the controlled switching circuit are operated so as to prevent a closed circuit path between either of the polarity reversal detection coupling circuit and the ring trip detection coupling circuit. In addition, the ring trip detection coupling circuit is disabled, while the line parameter measurement coupling circuit is enabled. Again, although the polarity reversing circuit continues to be driven, so that the tip and ring terminals of the SLIC are fed with the required polarity, a path thereto through the switching circuit is interrupted. Consequently, the capacitor is connected exclusively to the line measurement coupling circuit. This allows pulse-width timing required for a given loop length to be established by the value of the loop_voltage current source, the value of the reference voltage, and the value of the capacitor.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 diagrammatically illustrates an embodiment of a multi-function circuit of the present invention for selectively providing for soft polarity reversal detection, ring-trip filtering, and line voltage

measurement using a common capacitor;

Figure 2 shows a non-limiting implementation of a unidirectional switch that may be employed in each of the ring trip detection and line parameter measurement coupling circuits of the multi-function circuit of Figure 1; and

Figure 3 shows a non-limiting implementation of a bidirectional switch that may be employed in the controlled switching circuit of the multi-function circuit of Figure 1.

DETAILED DESCRIPTION

Before describing in detail the controllably switched, external component-coupling circuit architecture of the present invention, it should be observed that the invention resides primarily in prescribed modular arrangements of conventional communication circuits and associated signal processing components and attendant supervisory control circuitry therefor, that controls the operations of such circuits and components. In a practical implementation that facilitates their incorporation into existing subscriber line interface circuit cards of telecommunication equipment, these modular arrangements may be readily implemented as a field programmable application specific

integrated circuit (ASIC) chip sets.

Consequently, the configuration of such arrangements of circuits and components and the manner in which they are interfaced with other telecommunication equipment have, for the most part, been illustrated in the drawings by readily understandable block diagrams and reduced complexity circuit configurations, which show only those specific details that are pertinent to the present invention, so as not to obscure the disclosure with details which will be readily apparent to those skilled in the art having the benefit of the description herein. Thus, the block diagram and circuit illustrations are primarily intended to show the major components of the invention in a convenient functional grouping, whereby the present invention may be more readily understood.

Attention is now directed to Figure 1, which shows a reduced complexity schematic illustration of a single component-referenced, multi-function circuit of the present invention for the example of selectively connecting a single external capacitor for use with each of a polarity reversal detection circuit, a ring trip detection circuit, and a line parameter measurement circuit. As shown therein the invention includes a 'soft' polarity reversal coupling circuit 10, a ring trip detection coupling circuit 20 and a line parameter measurement coupling circuit 30, each of which is

connected in circuit with a controlled switching circuit 40. The controlled switching circuit 40 is terminated by a terminal 50 that is adapted to be connected to an externally programmed capacitor 60.

5 The polarity reversal coupling circuit 10 includes a first, differentially connected controlled device pair, shown as a pair of bipolar transistors 11 and 12, having their emitters coupled in common to a first (polarity reversal (PR)) current source 13, and their collectors
10 coupled to respective ports 14 and 15 of a polarity reversal detection circuit 100. Although the circuit implementation shown and described herein employs bipolar transistors, it is to be understood that the invention is not limited thereto, but also may be configured of
15 alternative equivalent circuit devices, such as field effect transistors (FETs), for example.

 The base of transistor 11 is coupled to a first (polarity reversal) voltage reference $V_{ref_{pr}}$, while the base of transistor 12 is coupled to a polarity reversal
20 current source 19 and to a first port 41 of the controlled switching circuit 40. With the dedicated current supply connection shown, the differential transistor pair 11/12 is continuously enabled and drives the polarity reversing circuit 100 for any of the three
25 functional states, so as to ensure that both tip and ring terminals of the SLIC will be fed with the required

polarity. Polarity reversal is defined by the polarity reversal current source 16 and the external capacitor 60.

It should be noted that the polarity reversal detection circuit 100, as well as a ring trip detection logic circuit 200, and a loop voltage output logic circuit 300 may be of a type employed in present day SLIC circuits. As such circuits, per se, are of conventional design and details of the same are not required for an understanding of the present invention, they are not shown in detail in Figure 1 and will not be described here. Instead, in order to maintain a concise description of the invention, the functions of such circuits will be referenced, as appropriate, in the description of the various modes of operation of the invention set forth below.

The ring trip detection coupling circuit 20 includes a second, differentially connected pair of transistors 21 and 22, having their emitters coupled in common to a second (ring trip (RT)) current source 28, and via a controlled unidirectional switch 24 to a voltage reference terminal (e.g., ground). As a non-limiting implementation, the switch 24, as well as a unidirectional switch 34 within the line parameter measurement coupling circuit 30 to be described, may be configured as a bipolar transistor switch shown in Figure 2.

As shown therein, the unidirectional switch comprises a bipolar transistor 25 having its collector-emitter path coupled in circuit between a switch output terminal 26 and a voltage reference terminal (e.g., ground), and its base, which is coupled to the voltage reference terminal via a bias resistor 27, is coupled to receive a switch (open/close) command signal. When turned on by an input command applied to its base, transistor 25 serves to sink current applied to its collector (output) terminal 26, and thereby provides a by-pass current flow path therethrough.

In Figure 1, the collectors of the differentially coupled transistors 21 and 22 of ring trip detection coupling circuit 20 are coupled to respective ports 23 and 24 of a ring trip detection logic circuit 200. The base of the transistor 21 is coupled to receive a second (RT) voltage reference $V_{ref_{rt}}$, while the base of transistor 22 is coupled to a second port 42 of the controlled switching circuit 40, to which a selectively enabled/disabled polarity reversal current source 29 is coupled.

The line parameter measurement coupling circuit 30 includes a third, differentially connected pair of transistors 31 and 32, whose emitters are coupled in common to a third (loop voltage (LV)) current source 33, and via a controlled switch 34 to a voltage reference

terminal (e.g., ground). The collectors of transistors 31 and 32 are coupled to respective ports 34 and 35 of a loop voltage output logic circuit 300. The base of transistor 31 is coupled to receive a third (LV) voltage reference VLL, while the base of transistor 32 is coupled to a third port 43 of the switching circuit 40. Port 43 is coupled to a selectively enabled/disabled loop_voltage current source 36 and to the terminal 50, to which capacitor 60 is coupled, as described above. When enabled, the line parameter measurement coupling circuit 30 measures pulse-width timing (which is representative of loop length) in accordance with the value of the loop_voltage current source 36, the value of reference voltage VLL and to the value of the capacitor 60.

15 The controlled switching circuit 40 contains a first controlled bidirectional switch 44 coupled between ports 41 and 42, and a second bidirectional controlled switch 45 coupled between ports 42 and 43. As a non-limiting example, each of switches 44 and 45 may be implemented as shown in Figure 3, to include a pair of bipolar transistors 51 and 52, having their emitters coupled in common to a DC current source 53, and their collectors coupled to respective input/output terminals 54/55 (corresponding to terminal pairs 41/42 and 42/43). The base of each transistor 51/52 is coupled to receive a reference bias DC current supplied by a current source

56, and to the collector-emitter current flow path of a bipolar transistor 57. Transistor 57 has its collector-emitter path coupled to a voltage reference terminal (e.g., ground), and its base, which is coupled to the voltage reference terminal via a bias resistor 58, is coupled to receive a switch (open/close) command signal. When turned-on by an input command applied to the base of transistor 57, the switch provides a bidirectional connectivity between input/output terminals 54/55 through the collector-emitter paths of turned-on transistors 51 and 52.

OPERATION

POLARITY REVERSAL DETECTION

Whenever the polarity reversal function is to be employed, each of the bidirectional switches 44 and 45 of the controlled switching circuit 40 is closed, so as to provide a closed circuit path between switching circuit ports 41 and 43 and thereby place the capacitor 60 in circuit with the base of transistor 12 of the differentially coupled pair of transistors 11 and 12 of the polarity reversal coupling circuit 10. In addition, the controlled unidirectional switches 24 and 34 within the ring trip detection coupling circuit 20 and the line parameter measurement coupling circuit 30 are closed, so as to provide bias current by-pass paths for the

differentially coupled transistors pairs and thereby disable each of coupling circuits 20 and 30. As noted above, the differential transistor pair 21/22 is continuously enabled and drives the polarity reversing circuit 100 to ensure that both tip and ring terminals of the SLIC are fed with the required polarity, with switching circuit 40 providing a connection path therethrough to capacitor 60. As a result, the application of a (soft) polarity reversal command will have its direction established by the polarity of the polarity reversal current source 16 supplied to port 41, and be sensed by the polarity reversal circuit 100, as intended.

RING TRIP DETECTION

15 During ringing mode, bidirectional switch 44 of the
controlled switching circuit 40 is opened, while its
switch 45 is closed, so as to provide a closed circuit
path between switching circuit port 42 and 43, and
thereby place the capacitor 60 in circuit with the base
20 of transistor 22 of the differentially coupled pair of
transistors 21 and 22 of the ring trip detection coupling
circuit 20. In addition, the controlled unidirectional
switch 24 within the ring trip detection coupling circuit
20 is opened, while the controlled switch 34 in the line
25 parameter measurement coupling circuit 30 is closed.

Since the controlled switch 34 in the line parameter measurement coupling circuit 30 is closed, a bias current by-pass path is provided for the differentially coupled transistor pair 31/32 in line parameter measurement coupling circuit 30, so that the line parameter measurement coupling circuit 30 is disabled. Although the differential transistor pair 21/22 continues to drive the polarity reversing circuit 100 so as to ensure that both tip and ring terminals of the SLIC are fed with the required polarity, as described above, the path through switch 44 of the switching circuit 40 is interrupted. Consequently, the capacitor 60 is now connected exclusively to the ring trip detection coupling circuit 20, to serve as a ring trip filter function.

15 LOOP VOLTAGE TEST MODE

During loop voltage test mode, each of the switches 44 and 45 of the controlled switching circuit 40 is opened, so as to prevent a closed circuit path between either of circuits 10 and 20 and the capacitor 60. In addition, the controlled unidirectional switch 24 within the ring trip detection coupling circuit 20 is closed, while the controlled switch 34 in the line parameter measurement coupling circuit 30 is opened. Since the controlled switch 24 in the ring trip coupling circuit 20

is closed, a bias current by-pass path is provided for the differentially coupled transistor pair 21/22 in the ring trip detection coupling circuit 20, so that the ring trip detection coupling circuit 20 is disabled. Also, 5 although the differential transistor pair 21/22 continues to drive the polarity reversing circuit 100 so that the tip and ring terminals of the SLIC are fed with the required polarity, the path through switch 44 of the switching circuit 40 is interrupted. Consequently, the 10 capacitor 60 is now connected exclusively to the line measurement coupling circuit 30. This allows pulse-width timing required for a given loop length to be established by the value of the loop_voltage current source 36, the value of reference voltage VLL, and the value of the 15 capacitor 60.

As will be appreciated from the foregoing description, the hardware complexity problem associated with the use of separate dedicated capacitors for 20 implementing multiple functions of a line card circuit, including soft polarity reversal of the tip and ring terminals, preventing false detection (ring trip) when ringing the phone, and providing a measure of the length of the wireline pair that connects the line card to the 25 phone, are significantly reduced in accordance with the controllably switched circuit architecture of the present

invention. As described above, the invention takes advantage of the fact that these three functions are not required nor do not they have to occur at the same time. As a result, each function can be implemented with the
5 same capacitor element, which is controllably switched in circuit with an associated signaling circuit for that function.

While I have shown and described an embodiment in
10 accordance with the present invention, it is to be understood that the same is not limited thereto but is susceptible to numerous changes and modifications as known to a person skilled in the art, and I therefore do not wish to be limited to the details shown and described
15 herein, but intend to cover all such changes and modifications as are obvious to one of ordinary skill in the art.

WHAT IS CLAIMED

1. A method of selectively enabling each of a plurality of respectively different circuits of a multi-function circuit arrangement to perform its respective signaling function, each respectively different circuit
5 requiring use of the same type of external circuit component to implement its respective signaling function, said method comprising the steps of:

(a) providing a single external circuit component that corresponds to said same type of external circuit
10 component; and

(b) in association with performance of each respectively different circuit function of said multi-function circuit arrangement,

(b1) enabling that one of said plurality of
15 respectively different circuits which performs said each circuit function, while selectively disabling one or more others of said plurality of respectively different circuits which do not perform said each circuit function, and

20 (b2) coupling said single external circuit component to said one of said plurality of respectively different circuits, while decoupling said single external circuit component from said one or more others of said plurality of respectively different circuits.

2. A method according to claim 1, wherein step (b2) comprises coupling said single external circuit component to said one of said plurality of respectively different circuits through a selectively controlled
5 switching circuit having a plurality ports respectively coupled to said plurality of respectively different circuits, and a component-coupling port coupled to said single external circuit component.

3. A method according to claim 1, wherein step (b1) comprises controllably enabling that one of said plurality of respectively different circuits which performs said each circuit function by means of a
5 differentially coupled transistor circuit.

4. A method according to claim 1, wherein said multi-function circuit arrangement comprises a telecommunication circuit card and said plurality of respectively different circuits include two or more of a
5 polarity reversal detection circuit, a ring-trip circuit, and a line voltage measurement circuit, and wherein said same type of external circuit component comprises a capacitor.

5. A method according to claim 4, wherein, during

operation of each of said ring-trip circuit and said line voltage measurement circuit,

step (b1) comprises enabling said polarity reversal
5 detection circuit, so that tip and ring terminals of said telecommunication circuit will be supplied with a required polarity voltage, and

step (b2) comprises decoupling said single external circuit component from said polarity reversal detection
10 circuit.

6. A method according to claim 4, wherein step (b1) includes continuously enabling said polarity reversal detection circuit irrespective of whether said polarity reversal detection circuit is to be coupled with
5 said capacitor in step (b2), but selectively enabling only that one of said ring-trip circuit and said line voltage measurement circuit that is to be coupled with said capacitor in step (b2).

7. A method according to claim 2, wherein said multi-function circuit arrangement comprises a telecommunication circuit card and said plurality of respectively different circuits include two or more of a
5 polarity reversal detection circuit, a ring-trip circuit, and a line voltage measurement circuit, said same type of external circuit component comprises a capacitor, and

wherein said polarity reversal detection circuit is coupled to a first one of said plurality of ports of said controlled switching circuit, and said ring-trip circuit and said line voltage measurement circuit are coupled to respective and second and third ones of said plurality of ports of said controlled switching circuit, that are intermediate said first one of said plurality of ports and said component-coupling port.

8. An apparatus for selectively enabling each of a plurality of respectively different circuits of a multi-function circuit device to perform its respective signaling function, each respectively different circuit requiring use of the same type of external circuit component to implement its respective signaling function, comprising:

a single connection terminal adapted to coupled to a single external circuit component that corresponds to said same type of external circuit component; and

a circuit interconnection arrangement which is operative, in association with performance of each respectively different circuit function of said multi-function circuit device, to enable that one of said plurality of respectively different circuits which performs said each circuit function, and to selectively disable one or more others of said plurality of

respectively different circuits which do not perform said
each circuit function, and to couple said single external
20 circuit component to said one of said plurality of
respectively different circuits, while decoupling said
single external circuit component from said one or more
others of said plurality of respectively different
circuits.

9. An apparatus according to claim 8, wherein said
circuit interconnection arrangement includes a
selectively controlled switching circuit having a
plurality ports respectively coupled to said plurality of
5 respectively different circuits, and a component-coupling
port coupled to said single external circuit component,
and which is operative to couple said single external
circuit component to said one of said plurality of
respectively different circuits.

10. An apparatus according to claim 8, wherein said
circuit interconnection arrangement includes a plurality
of differentially coupled transistor circuits, which are
coupled to said plurality of respectively different
5 circuits that perform said respectively different circuit
functions, and which are operative to selectively enable
or disable said respectively different circuits.

11. An apparatus according to claim 8, wherein said multi-function circuit arrangement comprises a telecommunication circuit card and said plurality of respectively different circuits include two or more of a polarity reversal detection circuit, a ring-trip circuit, and a line voltage measurement circuit, and wherein said same type of external circuit component comprises a capacitor.

12. An apparatus according to claim 11, wherein, during activation of each of said ring-trip circuit and said line voltage measurement circuit, said circuit interconnection arrangement is operative to enable said polarity reversal detection circuit, so that tip and ring terminals of said telecommunication circuit will be supplied with a required polarity voltage, and to decouple said single external circuit component from said polarity reversal detection circuit.

13. An apparatus according to claim 10, wherein said circuit interconnection arrangement is operative to continuously enable said polarity reversal detection circuit irrespective of whether said polarity reversal detection circuit is to be coupled with said capacitor, and to selectively enable only that one of said ring-trip

circuit and said line voltage measurement circuit that is to be coupled with said capacitor.

14. An apparatus according to claim 9, wherein said multi-function circuit device comprises a telecommunication circuit card and said plurality of respectively different circuits include two or more of a
5 polarity reversal detection circuit, a ring-trip circuit, and a line voltage measurement circuit, said same type of external circuit component comprises a capacitor, and wherein said polarity reversal detection circuit is coupled to a first one of said plurality of ports of said
10 controlled switching circuit, and said ring-trip circuit and said line voltage measurement circuit are coupled to respective and second and third ones of said plurality of ports of said controlled switching circuit, that are intermediate said first one of said plurality of ports
15 and said component-coupling port.

15. A method of selectively enabling each of a plurality of respectively different circuits of a multi-function circuit card to perform its respective signaling function, said plurality of respectively different
5 circuits including a polarity reversal detection circuit, a ring-trip circuit, and a line voltage measurement circuit, and wherein each respectively different circuit

requires the use of an external capacitor to implement
its respective signaling function, said method comprising
10 the steps of:

(a) providing a single connection port that is adapted to be coupled to said external capacitor; and

(b) in association with performance of each
respectively different circuit function of said multi-
15 function circuit arrangement,

(b1) enabling that one of said plurality of respectively different circuits which performs said each circuit function, while selectively disabling one or more others of said plurality of respectively different
20 circuits which do not perform said each circuit function,
and

(b2) coupling said single external capacitor to said one of said plurality of respectively different circuits, while decoupling said single external capacitor
25 from said one or more others of said plurality of respectively different circuits.

16. A method according to claim 15, wherein step (b2) comprises coupling said single external capacitor to said one of said plurality of respectively different circuits through a selectively controlled switching
5 circuit having a plurality ports respectively coupled to said plurality of respectively different circuits, and a

component-coupling port coupled to said single external capacitor.

17. A method according to claim 15, wherein step (b1) comprises controllably enabling that one of said plurality of respectively different circuits which performs said each circuit function by means of a
5 differentially coupled transistor circuit.

18. A method according to claim 15, wherein, during operation of each of said ring-trip circuit and said line voltage measurement circuit,

step (b1) comprises enabling said polarity reversal
5 detection circuit, so that tip and ring terminals of said telecommunication circuit will be supplied with a required polarity voltage, and

step (b2) comprises decoupling said single external capacitor from said polarity reversal detection circuit.

19. A method according to claim 15, wherein step (b1) includes continuously enabling said polarity reversal detection circuit irrespective of whether said polarity reversal detection circuit is to be coupled with
5 said capacitor in step (b2), but selectively enabling only that one of said ring-trip circuit and said line voltage measurement circuit that is to be coupled with

said capacitor in step (b2).

20. A method according to claim 16, wherein said polarity reversal detection circuit is coupled to a first one of said plurality of ports of said controlled switching circuit, and said ring-trip circuit and said
5 line voltage measurement circuit are coupled to respective and second and third ones of said plurality of ports of said controlled switching circuit, that are intermediate said first one of said plurality of ports and said component-coupling port.

ABSTRACT

A controllably switched arrangement selectively couples multiple circuit functions, such as reverse polarity detection, ring-trip and line voltage measurement, of a telecommunication card to the same external programming component (e.g., capacitor) on an as needed basis, by means of associated coupling circuits and a controlled switching circuit. The coupling circuits are controllably enabled/disabled in association with the particular circuit function required, while the controlled switching circuit selectively connects each coupling circuit with the external component. Through combined control of the coupling circuits and the switching circuit, respectively different circuit functions can be implemented with the same external component.

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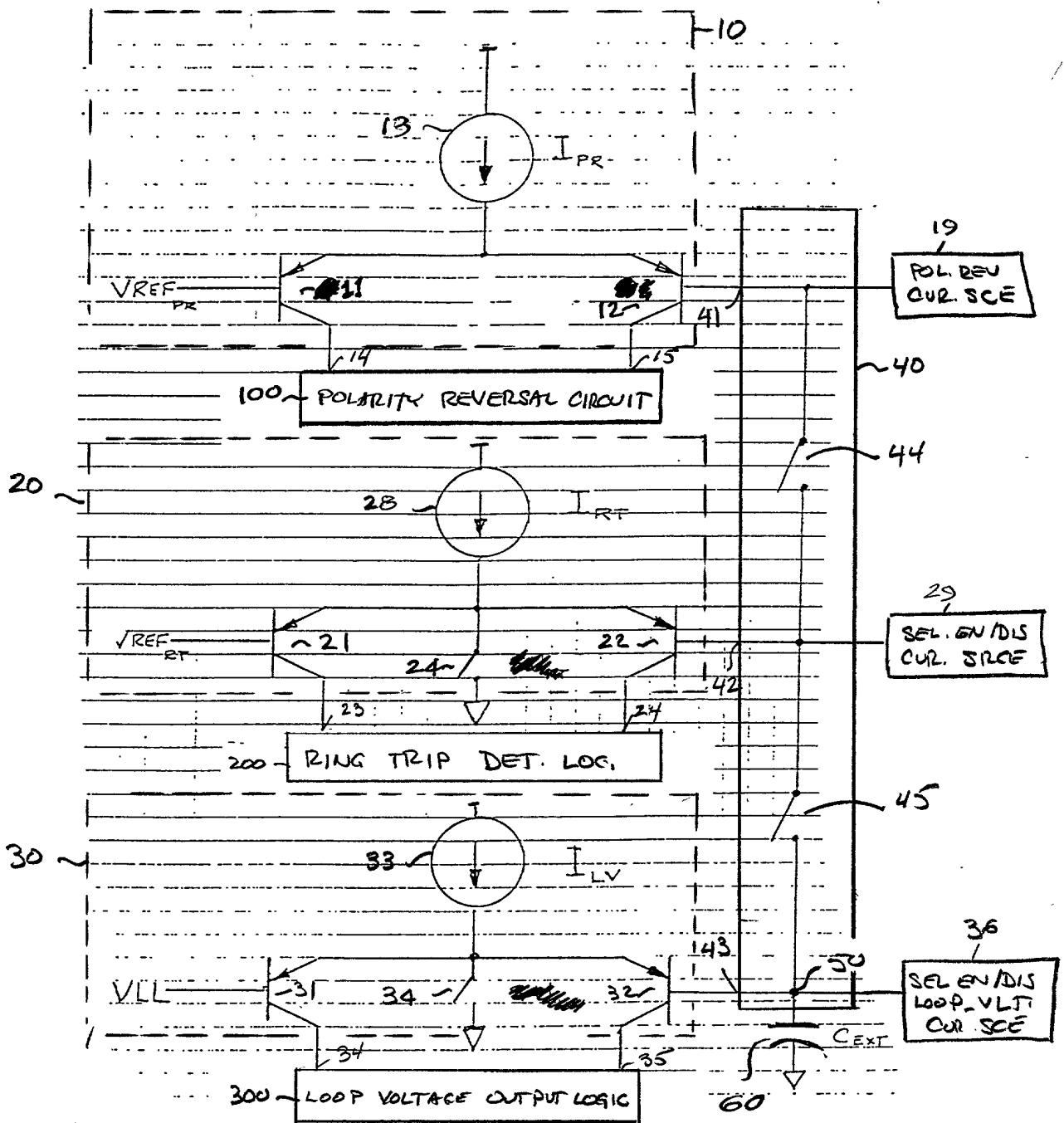


FIGURE 1

**DECLARATION AND POWER OF ATTORNEY FOR PATENT
APPLICATION**

Attorney Docket No.: 50089

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: CONTROLLED SWITCHING MECHANISM FOR ACCOMPLISHING SOFT POLARITY REVERSAL, RING-TRIP FILTERING, AND LINE VOLTAGE MEASUREMENTS IN A SUBSCRIBER LINE INTERFACE CIRCUIT USING COMMON CAPACITOR, the specification of which:

(check one)

 X is attached hereto

 was filed on

as Application Serial No.

and was amended on
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulation, 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the of the application on which priority is claimed:

Prior Foreign Application(s) Priority Claimed

<u> </u>	<u> </u>	<u> </u>	[]	[]
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
<u> </u>	<u> </u>	<u> </u>	[]	[]
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
<u> </u>	<u> </u>	<u> </u>	[]	[]
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, 120, of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

<u> </u>	<u> </u>	<u> </u>
(Appln Serial No.)	(Filing Date)	(Status)
(patented, pending, aban.)		
<u> </u>	<u> </u>	<u> </u>
(Appln Serial No.)	(Filing Date)	(Status)
(patented, pending, aban.)		

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

English Language Declaration

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

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